



(Use as many sheets as necessary)

Sheet		of
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Application Number	10/748,564
Filing Date	12/30/2003
First Named Inventor	Joseph B. Rowlands
Art Unit	2187
Examiner Name	none assigned R. Elmore
Attorney Docket Number	BP1833CON

[illegible]

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	†
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
RIE		EP Search Report for				
RIE		EP app. 02015677.4	02-04-2004	Broadcom Corp.		

**Examiner
Signature**

R. ΕΙΜΟΡΕ

Date
Considered

3-7-2005

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	2	of	2
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Complete if Known

Application Number	10/748,564
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Attorney Docket Number	BP1833CON

U. S. PATENT DOCUMENTS

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**Examiner
Signature**

R. ELMORE

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**Form PTO-1449** (modified)List of Patents and Publications
For Applicant's Information
Disclosure Statement
(Use several sheets if necessary)PATENT DKT. NO. **8P1833CON**APPLICANT: **JOSEPH B. ROWLANDS**FILING DATE: **12/30/03**SERIAL NO. **10/748,564**GROUP: **2141**
2187**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
RIE	A10	6,038,645	3/14/00	Nanda, et al.			

FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

RIE	A1	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm , January 15, 2001, 2 pages.
RIE	A2	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm , January 15, 2001, 3 pages.
RIE	A3	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian , January 15, 2001, 2 pages.
RIE	A4	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
RIE	A5	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.
RIE	A6	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
RIE	A7	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
RIE	A8	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
RIE	A9	Patterson and Hennessy, "Computer Architecture A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1990/1996, pp.375-378.

EXAMINER: **R. ELMORE**DATE CONSIDERED: **3-7-2005**

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Information Disclosure Statement--PTO 1449 (modified)